

LM137/LM337 3-Terminal Adjustable Negative Regulators

General Description

The LM137/LM337 are adjustable 3-terminal negative voltage regulators capable of supplying in excess of -1.5A over an output voltage range of -1.2V to -37V. These regulators are exceptionally easy to apply, requiring only 2 external resistors to set the output voltage and 1 output capacitor for frequency compensation. The circuit design has been optimized for excellent regulation and low thermal transients. Further, the LM137 series features internal current limiting, thermal shutdown and safe-area compensation, making them virtually blowout-proof against overloads. The LM137/LM337 serve a wide variety of applications including local on-card regulation, programmable-output voltage regulation or precision current regulation. The LM137/LM337 are ideal complements to the LM117/LM317 adjustable positive regulators.

Features

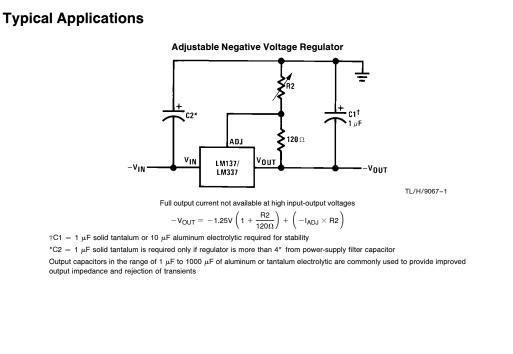
- Output voltage adjustable from -1.2V to -37V
- 1.5A output current guaranteed, -55°C to +150°C
- Line regulation typically 0.01%/V
- Load regulation typically 0.3%
- Excellent thermal regulation, 0.002%/W



- Excellent rejection of thermal transients
- 50 ppm/°C temperature coefficient
- Temperature-independent current limit
- Internal thermal overload protection
- P⁺ Product Enhancement tested
- Standard 3-lead transistor package
- Output is short circuit protected

LM137 Series Packages and Power Capability

Device	Package	Rated Power Dissipation	Design Load Current
LM137/337	TO-3 (K) TO-39 (H)	20W 2W	1.5A 0.5A
LM337	TO-220 (T)	15W	1.5A



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Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 4)

Power Dissipation	Internally Limited
Input-Output Voltage Differential	40V

Operating Junction Temperature Range				
LM137	-55°C to +150°C			
LM337	0°C to +125°C			
Storage Temperature	-65°C to +150°C			
Lead Temperature (Soldering, 10 sec.)	300°C			
Plastic Package (Soldering, 4 sec.)	260°C			
ESD Rating	2k Volts			

Electrical Characteristics (Note 1)

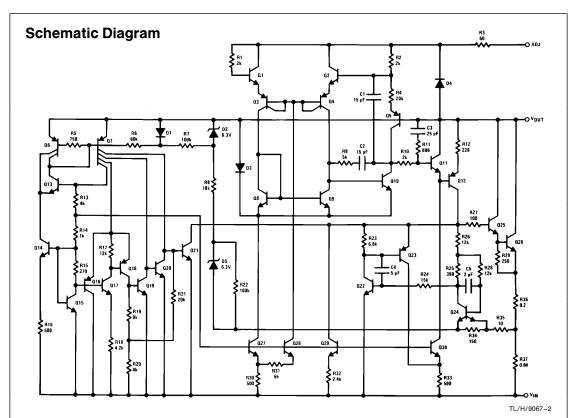
Parameter	Conditions	LM137			LM337			Units
		Min	Тур	Max	Min	Тур	Мах	
Line Regulation	$\begin{array}{l} T_{j}=25^{\circ}\text{C}, 3\text{V}\leq \left \text{V}_{IN}-\text{V}_{OUT}\right \leq 40\text{V}\\ (\text{Note 2)} \text{ I}_{L}=10 \text{ mA} \end{array}$		0.01	0.02		0.01	0.04	%/V
Load Regulation	T_{j} = 25°C, 10 mA \leq $I_{OUT} \leq$ I_{MAX}		0.3	0.5		0.3	1.0	%
Thermal Regulation	$T_j = 25^{\circ}C$, 10 ms Pulse		0.002	0.02		0.003	0.04	%/W
Adjustment Pin Current			65	100		65	100	μA
Adjustment Pin Current Charge	$ \begin{array}{l} 10 \text{ mA} \leq I_L \leq I_{MAX} \\ 3.0V \leq V_{IN} - V_{OUT} \leq 40V, \\ T_A = 25^\circ C \end{array} $		2	5		2	5	μA
Reference Voltage	$ \begin{array}{l} T_{j} = 25^{\circ}C \text{ (Note 3)} \\ 3V \leq \left V_{IN} - V_{OUT} \right \leq 40V \text{, (Note 3)} \\ 10 \text{ mA} \leq I_{OUT} \leq I_{MAX}, P \leq P_{MAX} \end{array} $		-1.250 -1.250			-1.250 -1.250		v v
Line Regulation	$3V \leq \left V_{IN} - V_{OUT} ight \leq$ 40V, (Note 2)		0.02	0.05		0.02	0.07	%/V
Load Regulation	10 mA \leq I _{OUT} \leq I _{MAX} , (Note 2)		0.3	1		0.3	1.5	%
Temperature Stability	$T_{MIN} \leq T_j \leq T_{MAX}$		0.6			0.6		%
Minimum Load Current	$\begin{aligned} V_{IN} - V_{OUT} &\leq 40V \\ V_{IN} - V_{OUT} &\leq 10V \end{aligned}$		2.5 1.2	5 3		2.5 1.5	10 6	mA mA
Current Limit	$\begin{split} V_{IN} - V_{OUT} &\leq 15V\\ K \text{ and T Package}\\ H \text{Package}\\ V_{IN} - V_{OUT} &= 40V, \text{T}_{j} = 25^{\circ}\text{C}\\ K \text{ and T Package}\\ H \text{Package} \end{split}$	1.5 0.5 0.24 0.15	2.2 0.8 0.4 0.17	3.5 1.8	1.5 0.5 0.15 0.10	2.2 0.8 0.4 0.17	3.7 1.9	A A A A
RMS Output Noise, % of VOUT	$T_{j} = 25^{\circ}$ C, 10 Hz \leq f \leq 10 kHz		0.003			0.003		%
Ripple Rejection Ratio	$V_{OUT} = -10V$, f = 120 Hz $C_{ADJ} = 10 \mu$ F	66	60 77		66	60 77		dB dB
Long-Term Stability	$T_{j} = 125^{\circ}C$, 1000 Hours		0.3	1		0.3	1	%
Thermal Resistance, Junction to Case	H Package K Package T Package		12 2.3	15 3		12 2.3 4	15 3	°C/W °C/W °C/W
Thermal Resistance, Junction to Ambient (No Heat Sink)	H Package K Package T Package		140 35			140 35 50		°C/W °C/W °C/W

Note 1: Unless otherwise specifications apply $-55^{\circ}C \le T_{j} \le +105^{\circ}C$ for the LM137, $0^{\circ}C \le T_{j} \le +125^{\circ}C$ for the LM337; $V_{IN} - V_{OUT} = 50$; and $I_{OUT} = 0.1A$ for the TO-39 package and $I_{OUT} = 0.5A$ for the TO-3 and TO-220 packages. Although power dissipation is internally limited, these specifications are applicable for power dissipations of 2W for the TO-39 and 20W for the TO-39 and TO-220. I_{MAX} is 1.5A for the TO-3 and TO-220 packages, and 0.2A for the TO-39 package.

Note 2: Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation. Load regulation is measured on the output pin at a point 1/6" below the base of the TO-3 and TO-39 packages.

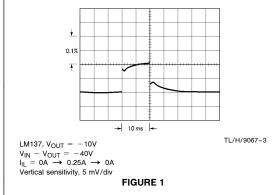
Note 3: Selected devices with tightened tolerance reference voltage available.

Note 4: Refer to RETS137H drawing for LM137H or RETS137K drawing for LM137K military specifications.

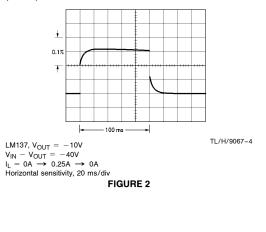


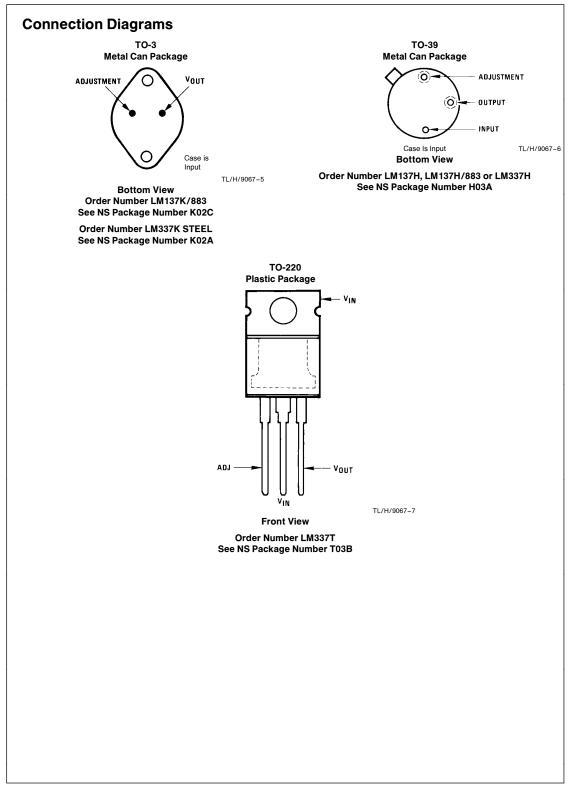
Thermal Regulation

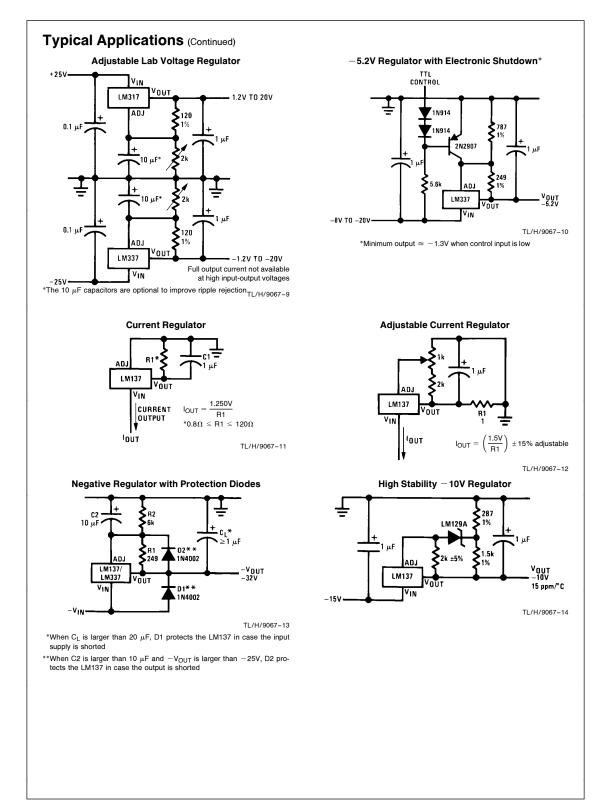
When power is dissipated in an IC, a temperature gradient occurs across the IC chip affecting the individual IC circuit components. With an IC regulator, this gradient can be especially severe since power dissipation is large. Thermal regulation is the effect of these temperature gradients on output voltage (in percentage output change) per Watt of power change in a specified time. Thermal regulation error is independent of electrical regulation or temperature coefficient, and occurs within 5 ms to 50 ms after a change in power dissipation. Thermal regulation depends on IC layout as well as electrical design. The thermal regulation of a voltage regulator is defined as the percentage change of V_{OUT} , per Watt, within the first 10 ms after a step of power is applied. The LM137's specification is 0.02%/W, max.

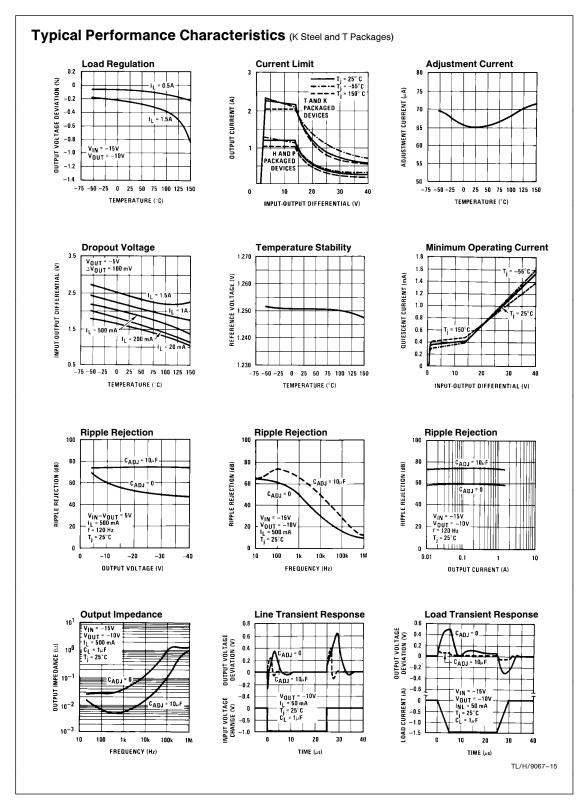


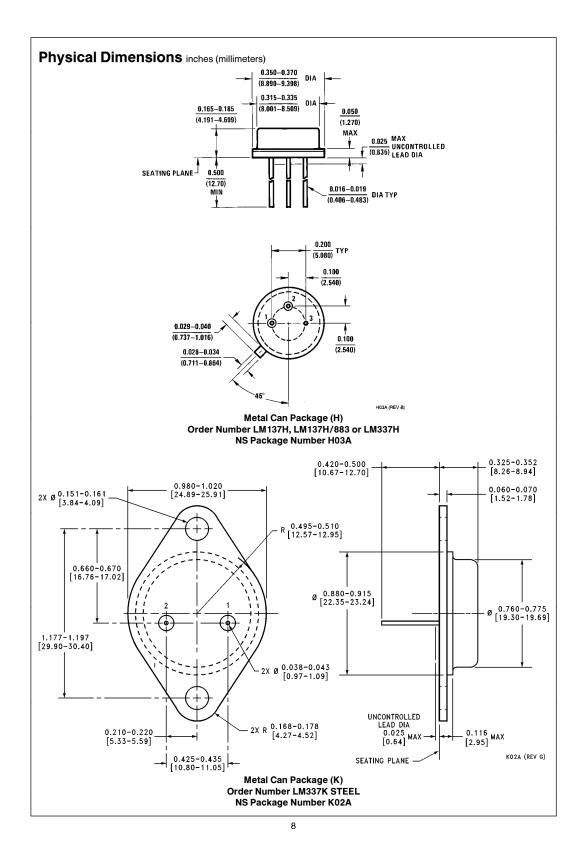
In *Figure 1*, a typical LM137's output drifts only 3 mV (or 0.03% of $V_{OUT} = -10V$) when a 10W pulse is applied for 10 ms. This performance is thus well inside the specification limit of 0.02%/W × 10W = 0.2% max. When the 10W pulse is ended, the thermal regulation again shows a 3 mV (o.08%) is additional to the thermal regulation error. In *Figure 2*, when the 10W pulse is applied for 100 ms, the output drifts only slightly beyond the drift in the first 10 ms, and the thermal error stays well within 0.1% (10 mV).

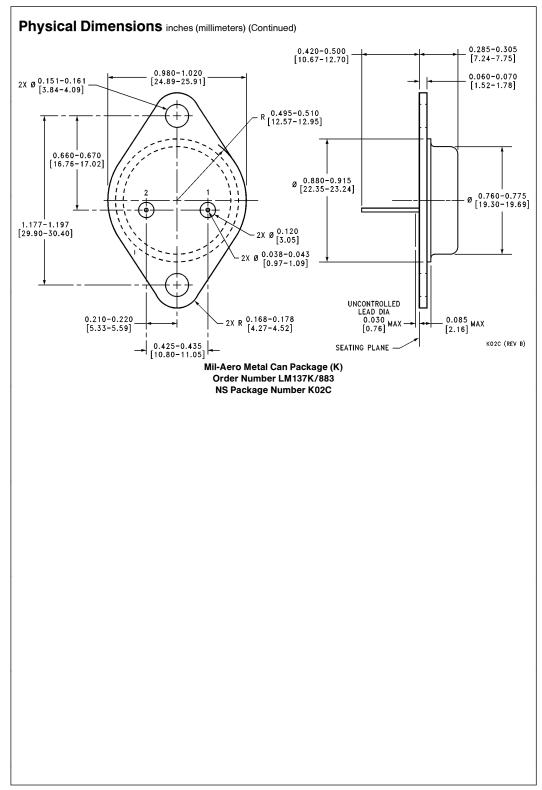


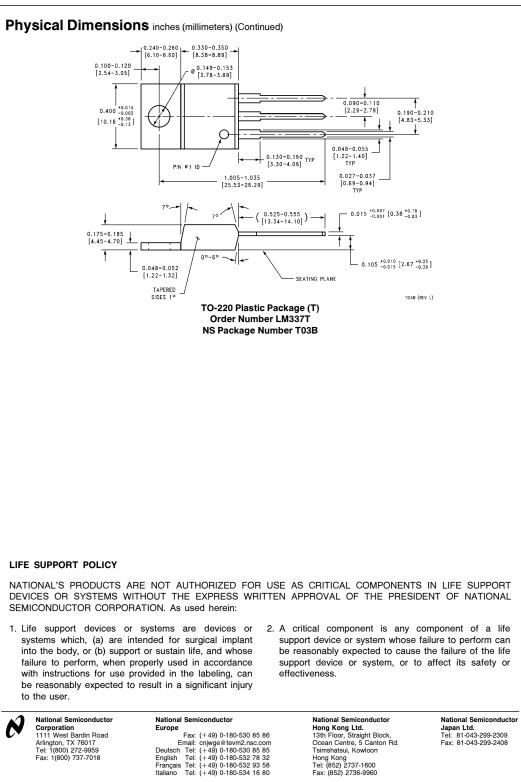












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