## PF0210

## MOS FET Power Amplifier Module for ADC Mobile Phone

## HITACHI

ADE-208-102E (Z)
Preliminary 6th. Edition
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## Features

- High efficiency: $34 \%$ Typ for CW
$30 \%$ Typ for $\pi / 4-$ DQPSK
- Low input power: 0 dBm ave. Typ for $\pi / 4$-DQPSK
- Simple bias circuit
- High speed switching: $8 \mu$ s Typ


## Pin Arrangement



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## Internal Diagram and External Circuit


$\mathrm{C} 1=\mathrm{C} 2=0.01 \mathrm{mF}$ (Ceramic chip capacitor)
$\mathrm{C} 3=330 \mathrm{mF}$ (Aluminum Electrolyte Capacitor)
$\mathrm{FB}=$ Ferrite bead BL01RN1-A62-001 (Manufacture: MURATA) or equivalent
$\mathrm{Z} 1=\mathrm{Z} 2=50 \mathrm{~W}$ (Microstrip line)

Absolute Maximum Ratings ( $\mathrm{Tc}=258 \mathrm{C}$ )

| Item | Symbol | Rating | Unit |
| :--- | :--- | :--- | :--- |
| Supply voltage | $\mathrm{V}_{\mathrm{DD}}$ | 17 | V |
| Supply current | $\mathrm{I}_{\mathrm{DD}}$ | 4 | A |
| $\mathrm{~V}_{\text {APC }}$ voltage | $\mathrm{V}_{\mathrm{APC}}$ | 5.5 | V |
| Input power | Pin | 20 | mW |
| Operating case temperature | $\mathrm{Tc}(\mathrm{op})$ | -30 to +100 | 8 C |
| Storage temperature | Tstg | -40 to +110 | 8 C |

Electrical Characteristics $(\mathrm{Tc}=258 \mathrm{C})$

## Analog Transmission

| Item | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Frequency | f | 824 | - | 849 | MHz | - |
| Drain cutoff current | $\mathrm{I}_{\mathrm{DS}}$ | - | - | 500 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{DD}}=17 \mathrm{~V}, \mathrm{~V}_{\text {APC }}=0 \mathrm{~V}$ |
| Total efficiency(1) | $\eta_{T}(1)$ | 30 | 34 | - | \% | $\begin{aligned} & \text { Pin }=3 \mathrm{dBm}, \mathrm{~V}_{\mathrm{DD}}=12.5 \mathrm{~V}, \\ & \text { Pout }=6 \mathrm{~W}\left(\mathrm{~V}_{\mathrm{APC}} \text { controlled }\right), \end{aligned}$ |
| $2^{\text {nd }}$ harmonic distortion | 2nd H.D. | - | -50 | -30 | dBc |  |
| 3rd harmonic distortion | 3rd H.D. | - | -60 | -40 | dBc |  |
| Input VSWR | VSWR (in) | - | 2 | 3 | - |  |
| Output power | Pout | 6 | 12 | - | W | $\begin{aligned} & \text { Pin }=3 \mathrm{dBm}, \mathrm{~V}_{\mathrm{DD}}=12.5 \mathrm{~V}, \\ & \mathrm{~V}_{\mathrm{APC}}=4 \mathrm{~V} \end{aligned}$ |
| Isolation | - | - | -50 | -40 | dBm | $\begin{aligned} & \text { Pin }=3 \mathrm{dBm}, \mathrm{~V}_{\mathrm{DD}}=12.5 \mathrm{~V}, \\ & \mathrm{~V}_{\mathrm{APC}}=0.5 \mathrm{~V} \end{aligned}$ |
| Stability | - | No parasitic oscillation |  |  | - | $\begin{aligned} & \text { Pin }=3 \mathrm{dBm}, \mathrm{~V}_{\mathrm{DD}}=12.5 \mathrm{~V}, \\ & \text { Pout } \leq 6 \mathrm{~W}, \\ & \text { Output VSWR }=20: 1 \text { All phases } \end{aligned}$ |

## Digital Transmission

| Item | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Frequency | f | 824 | - | 849 | MHz | - |
| Total efficiency(2) | $\eta_{T}(2)$ | 25 | 30 | - | \% | Pin controlled ( $\pi / 4$-DQPSK, $\mid \alpha=$ $0.35,48.6 \mathrm{kbps}$ ), BW $=24.3 \mathrm{kHz}$ with Root Nyquist Filter, Pout $=5.5$ W ave., $\mathrm{V}_{\mathrm{DD}}=12.5 \mathrm{~V} \mathrm{~V}_{\mathrm{APC}}=3.9 \mathrm{~V}$ |
| Adjacent channel leakage power | $\mathrm{P}_{\text {ADJ }}(30 \mathrm{k})$ | - | -30 | -28 | dBc |  |
|  | $\overline{P_{\text {ADJ }}(60 \mathrm{k})}$ | - | -50 | -46 | dBc |  |
| Input power | Pin | - | - | 5 | dBm ave. |  |

## Mechanical Characteristics

| Item | Conditions | Spec |
| :--- | :--- | :--- |
| Torque for screw up the heatsink flange | M3 Screw Bolts | 4 to $6 \mathrm{~kg} \cdot \mathrm{~cm}$ |
| Warp size of the heatsink flange: S |  | $\mathrm{S}=0$ |
|  |  | $+0.3 /-0 \mathrm{~mm}$ |

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## Characteristics Curve






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Package Dimensions Unit: mm


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