

## 74VHCU04 Hex Inverter

### General Description

The VHCU04 is an advanced high speed CMOS Inverter fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Since the internal circuit is composed of a single stage inverter, it can be used in analog applications such as crystal oscillators. An input protection circuit ensures that 0V to 7V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V systems and two supply systems such as battery

backup. This circuit prevents device destruction due to mismatched supply and input voltages.

### Features

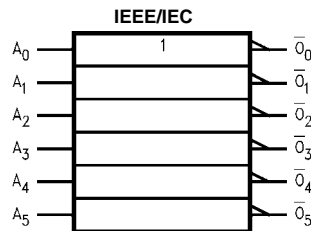
- High Speed:  $t_{PD} = 3.5$  ns (typ) at  $V_{CC} = 5$  V
- Low Power Dissipation:  $I_{CC} = 2$   $\mu$ A (Max) @  $T_A = 25^\circ$ C
- High Noise Immunity:  $V_{NIH} = V_{NIL} = 28\%$   $V_{CC}$  (Min)
- Power down protection is provided on all inputs
- Low Noise:  $V_{OLP} = 0.8$  V (Max)
- Pin and Function Compatible with 74HCU04

### Ordering Code:

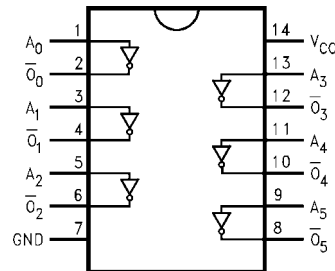
Order Number	Package Number	Package Description
74VHCU04M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
74VHCU04SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74VHCU04MTC	MTC14	14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide
74VHCU04N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Surface mount packages are also available on Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

### Logic Symbol



### Connection Diagram



### Pin Descriptions

Pin Names	Description
$A_n$	Inputs
$\bar{O}_n$	Outputs

### Truth Table

A	$\bar{O}$
L	H
H	L

**Absolute Maximum Ratings** (Note 1)

Supply Voltage ( $V_{CC}$ )	-0.5V to +7.0V
DC Input Voltage ( $V_{IN}$ )	-0.5V to +7.0V
DC Output Voltage ( $V_{OUT}$ )	-0.5V to $V_{CC} + 0.5V$
Input Diode Current ( $I_{IK}$ )	-20 mA
Output Diode Current ( $I_{OK}$ )	$\pm 20$ mA
DC Output Current ( $I_{OUT}$ )	$\pm 25$ mA
DC $V_{CC}$ /GND Current ( $I_{CC}$ )	$\pm 50$ mA
Storage Temperature ( $T_{STG}$ )	-65°C to +150°C
Lead Temperature ( $T_L$ )	260°C
(Soldering, 10 seconds)	

**Recommended Operating Conditions** (Note 2)

Supply Voltage ( $V_{CC}$ )	2.0V to +5.5V
Input Voltage ( $V_{IN}$ )	0V to +5.5V
Output Voltage ( $V_{OUT}$ )	0V to $V_{CC}$
Operating Temperature ( $T_{OPR}$ )	-40°C to +85°C

**Note 1:** Absolute Maximum Ratings are values beyond which the device may be damaged or have its useful life impaired. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation outside databook specifications.

**Note 2:** Unused inputs must be held HIGH or LOW. They may not float.

**DC Electrical Characteristics**

Symbol	Parameter	$V_{CC}$ (V)	$T_A = 25^\circ\text{C}$			$T_A = -40^\circ\text{C to } +85^\circ\text{C}$		Units	Conditions
			Min	Typ	Max	Min	Max		
$V_{IH}$	HIGH Level	2.0	1.70			1.70		V	
	Input Voltage	3.0 – 5.5	0.8 $V_{CC}$			0.8 $V_{CC}$			
$V_{IL}$	LOW Level	2.0			0.30		0.30	V	
	Input Voltage	3.0 – 5.5			0.20 $V_{CC}$		0.20 $V_{CC}$		
$V_{OH}$	HIGH Level	2.0	1.8	2.0		1.8		V	$V_{IN} = V_{IL}$ $I_{OH} = -50 \mu\text{A}$
		3.0	2.7	3.0		2.7			
	Output Voltage	4.5	4.0	4.5		4.0		V	$V_{IN} = \text{GND}$ $I_{OH} = -4 \text{ mA}$ $I_{OH} = -8 \text{ mA}$
		3.0	2.58			2.48			
$V_{OL}$	LOW Level	2.0		0.0	0.2		0.2	V	$V_{IN} = V_{IH}$ $I_{OL} = 50 \mu\text{A}$
		3.0		0.0	0.3		0.3		
	Output Voltage	4.5		0.0	0.5		0.5	V	$V_{IN} = V_{CC}$ $I_{OL} = 4 \text{ mA}$ $I_{OL} = 8 \text{ mA}$
		3.0			0.36		0.44		
4.5				0.36		0.44			
$I_{IN}$	Input Leakage Current	0 – 5.5			$\pm 0.1$	$\pm 1.0$	$\mu\text{A}$	$V_{IN} = 5.5V$ or GND	
$I_{CC}$	Quiescent Supply Current	5.5			2.0	20.0	$\mu\text{A}$	$V_{IN} = V_{CC}$ or GND	

## Noise Characteristics

Symbol	Parameter	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C		Units	Conditions
			Typ	Limits		
V <sub>OLP</sub> (Note 3)	Quiet Output Maximum Dynamic V <sub>OL</sub>	5.0	0.5	0.8	V	C <sub>L</sub> = 50 pF
V <sub>OLV</sub> (Note 3)	Quiet Output Minimum Dynamic V <sub>OL</sub>	5.0	-0.5	-0.8	V	C <sub>L</sub> = 50 pF
V <sub>IHD</sub> (Note 3)	Minimum HIGH Level Dynamic Input Voltage	5.0		4.0	V	C <sub>L</sub> = 50 pF
V <sub>ILD</sub> (Note 3)	Maximum LOW Level Dynamic Input Voltage	5.0		1.0	V	C <sub>L</sub> = 50 pF

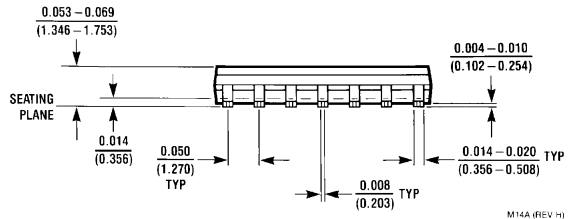
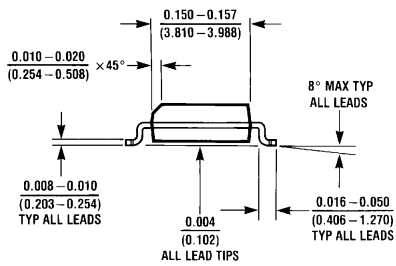
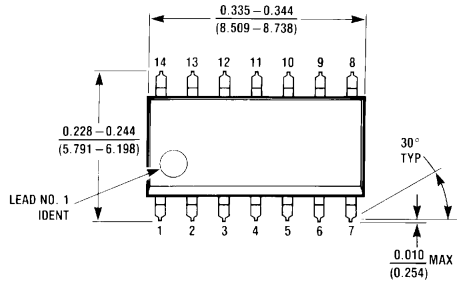
**Note 3:** Parameter guaranteed by design.

## AC Electrical Characteristics

Symbol	Parameter	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			T <sub>A</sub> = -40°C to +85°C		Units	Conditions
			Min	Typ	Max	Min	Max		
t <sub>PHL</sub>	Propagation Delay	3.3 ± 0.3		5.0	8.9	1.0	10.5	ns	C <sub>L</sub> = 15 pF
t <sub>PLH</sub>				7.5	11.4	1.0	13.0		C <sub>L</sub> = 50 pF
		5.0 ± 0.5		3.5	5.5	1.0	6.5	ns	C <sub>L</sub> = 15 pF
				5.0	7.0	1.0	8.0		C <sub>L</sub> = 50 pF
C <sub>IN</sub>	Input Capacitance			5	10		10	pF	V <sub>CC</sub> = Open
C <sub>PD</sub>	Power Dissipation Capacitance			9				pF	(Note 4)

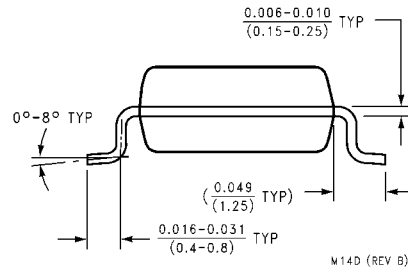
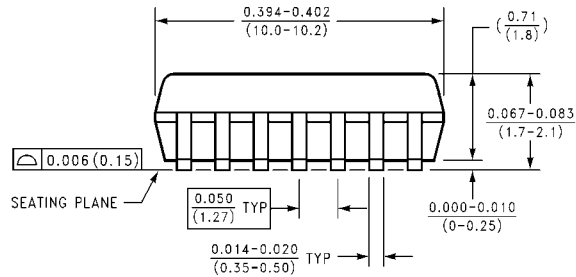
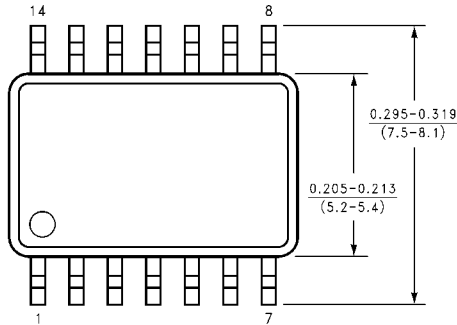
**Note 4:** C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC</sub> (opr.) = C<sub>PD</sub> \* V<sub>CC</sub> \* f<sub>IN</sub> + I<sub>CC</sub>/6 (per gate).

**Physical Dimensions** inches (millimeters) unless otherwise noted



M14A (REV H)

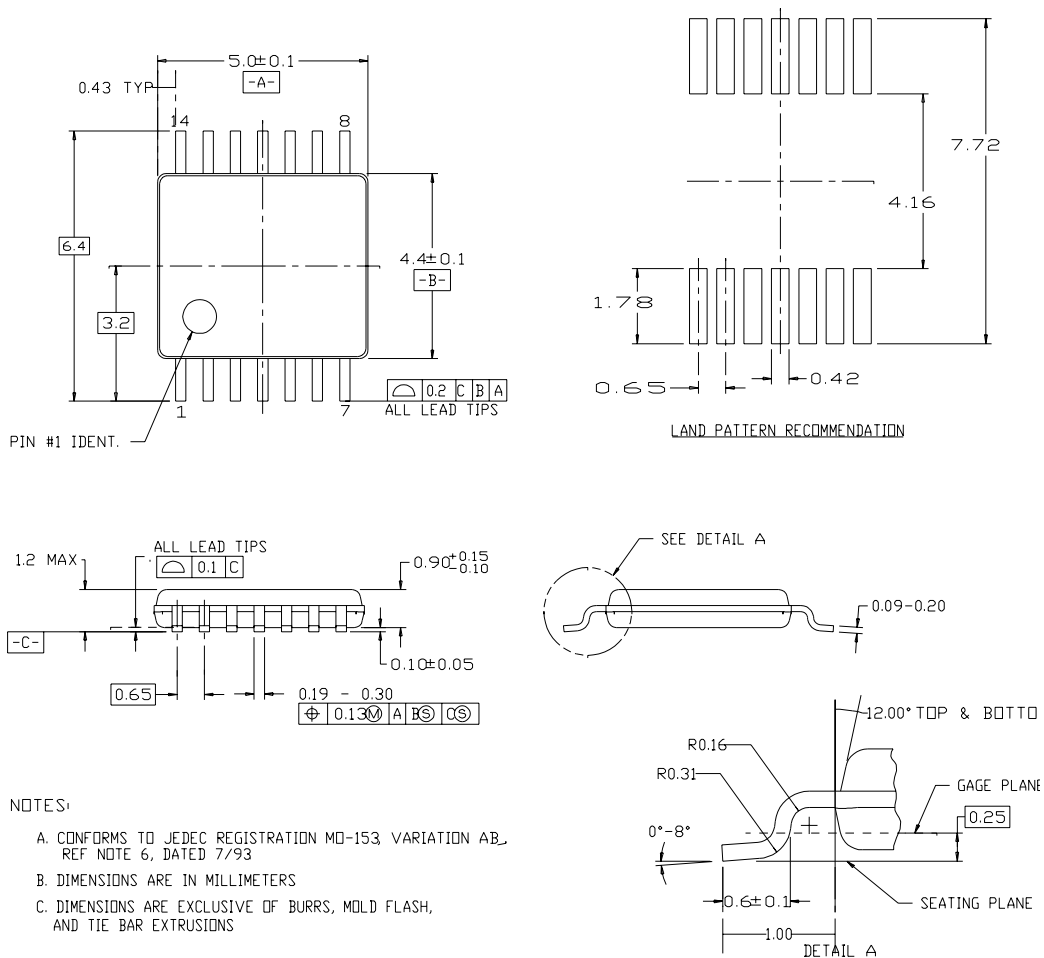
**14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow  
Package Number M14A**



M14D (REV B)

**14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide  
Package Number M14D**

**Physical Dimensions** inches (millimeters) unless otherwise noted (Continued)



- NOTES:
- A. CONFORMS TO JEDEC REGISTRATION MO-153, VARIATION AB, REF NOTE 6, DATED 7/93
  - B. DIMENSIONS ARE IN MILLIMETERS
  - C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS

**14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide  
Package Number MTC14**

**Physical Dimensions** inches (millimeters) unless otherwise noted (Continued)



**LIFE SUPPORT POLICY**

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

[www.fairchildsemi.com](http://www.fairchildsemi.com)