



MOTOROLA SEMICONDUCTORS

P.O. BOX 20912 • PHOENIX, ARIZONA 85036

MRF2010M

The RF Line

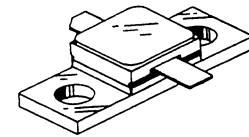
NPN SILICON MICROWAVE POWER TRANSISTOR

... designed for Class B and C *common base* broadband amplifier applications in the 1.7 to 2.3 GHz frequency range.

- Internal Input Matching for Broadband Operation
- Guaranteed Performance @ 2 GHz, 24 Vdc
Output Power = 10 Watts
Minimum Gain = 7.0 dB
- 100% Tested for Load Mismatch at All Phase Angles with 10:1 VSWR
- Hermetically Sealed Industry Standard Package
- Gold Metallized, Emitter Ballasted for Long Life and Resistance to Metal Migration
- Silicon Nitride Passivation
- Characterized for Operation from 20 V to 28 V Supply Voltages

10 W 2 GHz
MICROWAVE POWER
TRANSISTOR

NPN SILICON



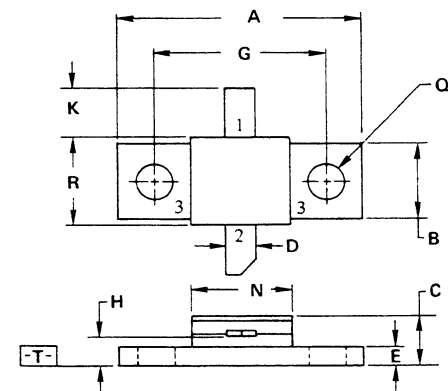
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	20	Vdc
Collector-Base Voltage	V_{CBO}	45	Vdc
Emitter-Base Voltage	V_{EBO}	3.5	Vdc
Collector-Current — Continuous	I_C	2.0	Adc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ (1) Derate above 25°C	P_D	35 200	Watts mW/ $^\circ\text{C}$
Storage Temperature Range	T_{stg}	-65 to +200	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (2)	$R_{\theta JC}$	5.0	$^\circ\text{C}/\text{W}$

- (1) These devices are designed for RF operation. The total device dissipation rating applies only when the devices are operated as RF amplifiers.
 (2) Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.



NOTES:

- DIMENSIONS \boxed{A} AND \boxed{B} ARE DATUMS.
- POSITIONAL TOLERANCE FOR MOUNTING HOLES:
 $\boxed{\varnothing .13(0.005) \text{ T A } \textcircled{B} \textcircled{M}}$
- \boxed{T} IS SEATING PLANE.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5, 1973.

STYLE 1:

- PIN 1. EMITTER
- COLLECTOR
- BASE

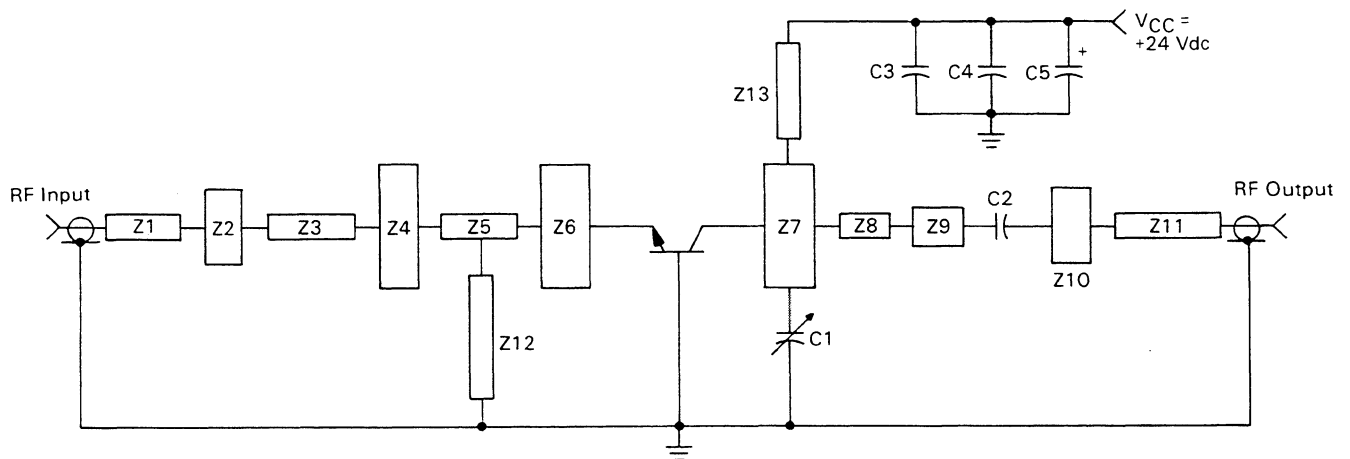
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	20.07	20.57	0.790	0.810
B	6.22	6.48	0.245	0.255
C	3.68	4.06	0.145	0.160
D	2.29	2.79	0.090	0.110
E	1.42	1.73	0.056	0.068
G	14.27 BSC		0.560 BSC	
H	2.29	2.79	0.090	0.110
K	3.43	4.19	0.135	0.165
N	7.87	8.38	0.310	0.330
Q	3.05	3.30	0.120	0.130
R	7.24	7.49	0.285	0.295

CASE 337-02

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage ($I_C = 20 \text{ mAdc}, I_B = 0$)	BV_{CEO}	20	—	—	Vdc
Collector-Emitter Breakdown Voltage ($I_C = 20 \text{ mAdc}, V_{BE} = 0$)	BV_{CES}	45	—	—	Vdc
Collector-Base Breakdown Voltage ($I_C = 20 \text{ mAdc}, I_E = 0$)	BV_{CBO}	45	—	—	Vdc
Emitter-Base Breakdown Voltage ($I_E = 2.0 \text{ mAdc}, I_C = 0$)	BV_{EBO}	3.5	—	—	Vdc
Collector Cutoff Current ($V_{CB} = 28 \text{ Vdc}, I_E = 0$)	I_{CBO}	—	—	2.0	mAdc
ON CHARACTERISTICS					
DC Current Gain ($I_C = 500 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$)	h_{FE}	10	—	100	—
DYNAMIC CHARACTERISTICS					
Output Capacitance ($V_{CB} = 24 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)	C_{ob}	—	14	18	pF
FUNCTIONAL TESTS					
Common-Base Amplifier Power Gain ($V_{CC} = 24 \text{ Vdc}, P_{out} = 10 \text{ W}, f = 2.0 \text{ GHz}$)	G_{PB}	7.0	8.0	—	dB
Collector Efficiency ($V_{CC} = 24 \text{ Vdc}, P_{out} = 10 \text{ W}, f = 2.0 \text{ GHz}$)	η	35	40		
Load Mismatch ($V_{CC} = 24 \text{ Vdc}, P_{out} = 10 \text{ W}, f = 2.0 \text{ GHz},$ $VSWR = 10:1$ All Phase Angles)	ψ	No Degradation in Power Output			

FIGURE 1 — 2 GHz TEST CIRCUIT



- Z1-Z13 — Microstrip, See Photomaster, Figure 8.
- C2, C3 — 68 pF Chip Capacitor
- C4 — 0.1 μF
- C5 — 10 μF , 35 V
- C1 — 0.6-4.5 pF Johanson 7271
- Board Material — 0.0312" Teflon Fiberglass
 $\epsilon_r = 2.5 \pm 0.05$



FIGURE 2 — OUTPUT POWER versus INPUT POWER
(f = 1.7 GHz)

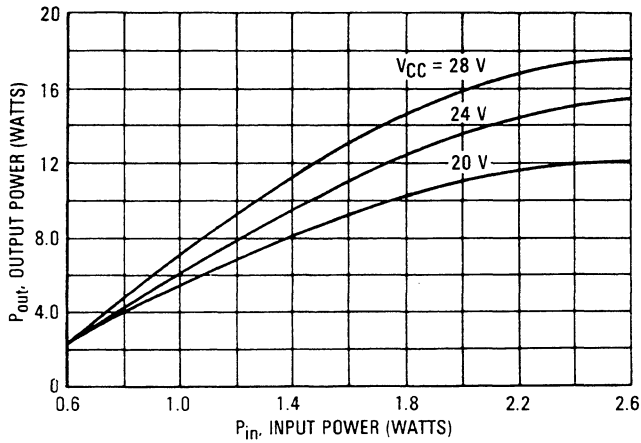


FIGURE 3 — OUTPUT POWER versus INPUT POWER
(f = 2.0 GHz)

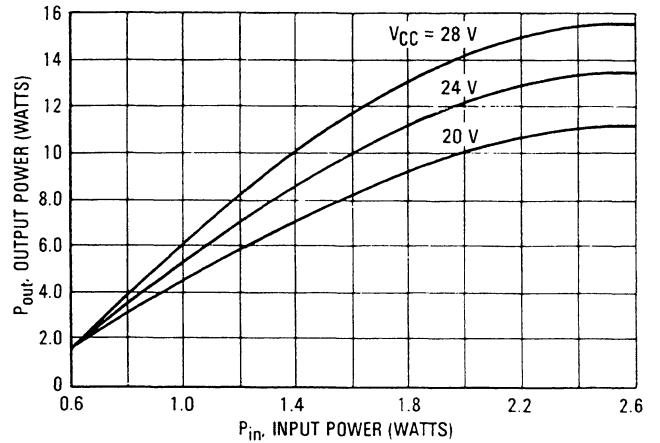


FIGURE 4 — OUTPUT POWER versus INPUT POWER
(f = 2.3 GHz)

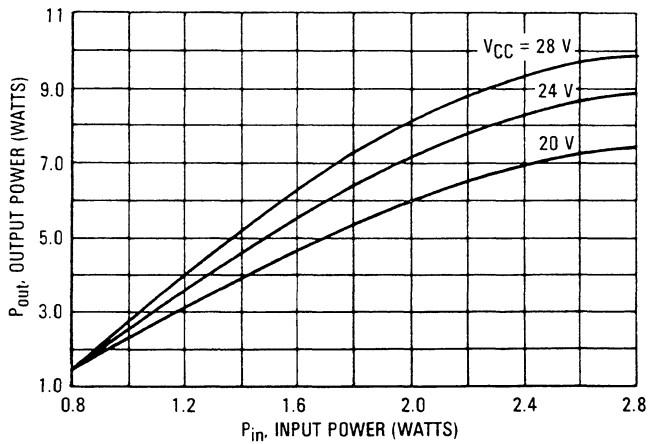


FIGURE 5 — POWER GAIN versus FREQUENCY

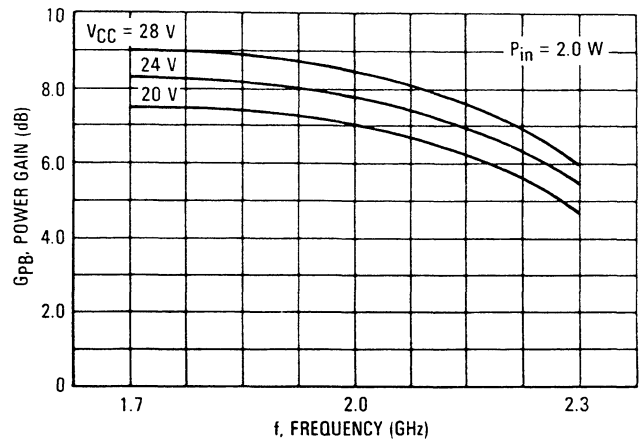
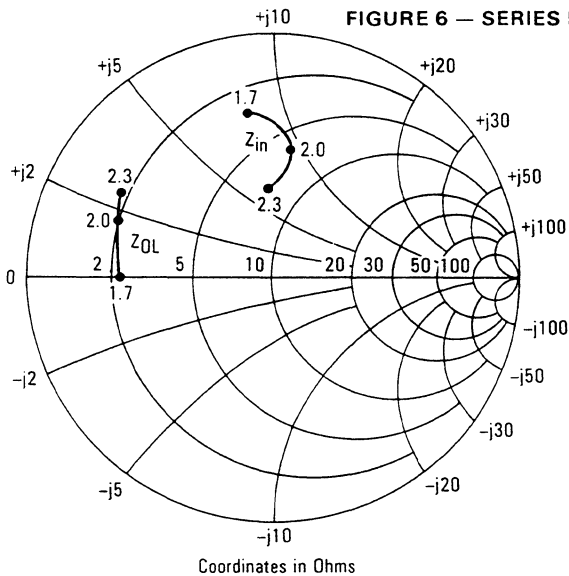


FIGURE 6 — SERIES EQUIVALENT INPUT/OUTPUT IMPEDANCE



$V_{CC} = 24$ V, $P_{in} = 2.0$ W

f GHz	Z_{in} Ohms	Z_{OL}^* Ohms
1.7	$3.5 + j8.0$	$2.3 + j0$
2.0	$7.0 + j9.5$	$2.0 + j1.6$
2.3	$8.0 + j6.5$	$1.8 + j2.2$

* Z_{OL} = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage, and frequency.



FIGURE 7 — 2 GHz TEST AMPLIFIER

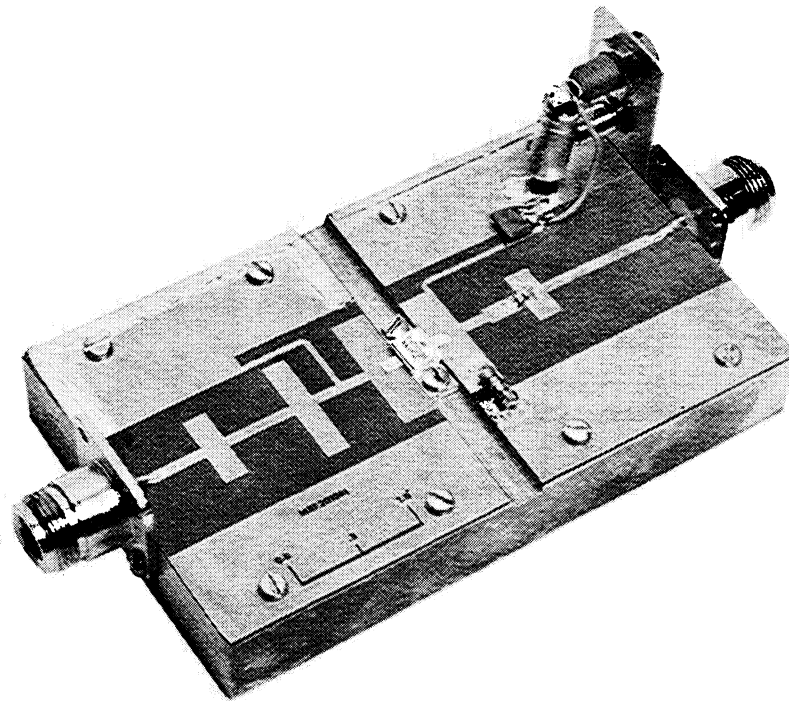
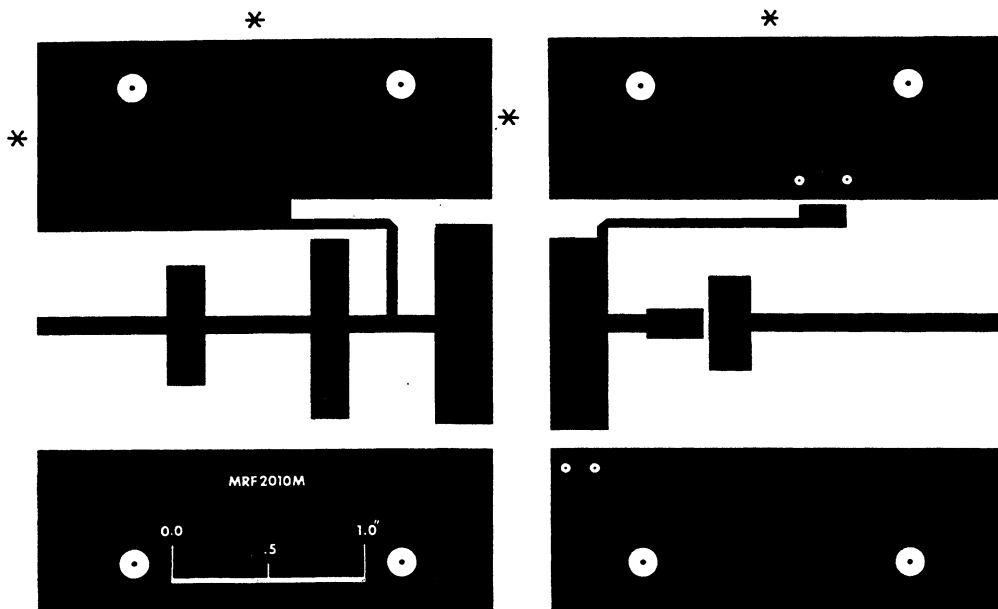


FIGURE 8 — 2 GHz TEST CIRCUIT PHOTOMASTER



- ⊙ Denotes Eyelet
- ⊖ Denotes 4-40 Screw Placement
- * Foil Wrap to Bottom Ground Plane

Scale 1:1

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