For complete installation instructions, please see the WTBDMS08 users manual on the included CD.

1) Install Freescale's CodeWarrior for HC(S)08 microcontrollers v6.0 or newer on the host computer. A free version can be found at http://www.freescale.com/codewarrior/downloads.

2) Copy the opensourcebdm.dll file from the included CD to the Freescale Codewarrior "gdi" directory (typical location is: C:\Program Files\Freescale\CodeWarrior for Microcontrollers V6.2\prog\gdi). If a file with the same name already exists, rename it to something like "opensourcebdm_old.dll, in case it is ever needed.

3) Set JP1 for the proper target voltage on the WTBDMS08.
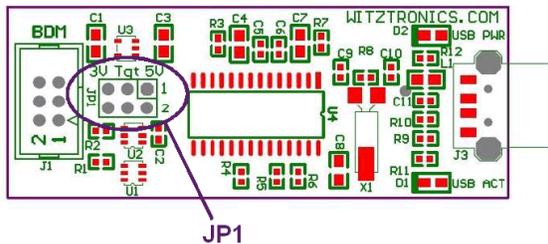
Target Voltage	JP1
5.0V	1-2
Provided by Target	3-4
3.3V	5-6

Note: The WTBDMS08 is an electrostatic sensitive device and handling precautions should be observed.

4) Plug the WTBDMS08 into a USB port on a host.

5) Install Select "No, not this time" on the initial *Found New Hardware Wizard* dialog box.

6) Select "Install software automatically" on the second *Found New Hardware Wizard* dialog box.

7) Connect the WTBDMS08 to the BDM program/debug header on the target PCB.

8) Create or open a project using Freescale's CodeWarrior tools.

9) Select the appropriate target as "HCS08 Open Source BDM" or "Cfv1 Open Source BDM" for a S08 or Cfv1 device respectively. This can also be changed in the project manager window.

10) Create and compile the target firmware.

11) Launch the Debugger

12) When starting the debugger you will be presented with a new dialog box. After making your selections and closing this dialog box, debugging will proceed as usual.

The software controlled Vdd options are not supported with the current WTBDMS08 hardware. See step 3 for Vdd options.

See reverse side for more information

- **Connection Control**
  - Automatically re-connect - This option causes the BDM to continuously update the interface speed to prevent loss of communication in the case with some devices. This option has no effect if the SYNC feature is not available on the target.

    Force BDM Clock Source - Some HCS08 or Coldfire V1 targets provide an alternative BDM clock selection which may be at a lower speed that the default. This can have advantages when connecting to high speed targets. There may be other requirements before the alternative clock will be used.

  - Use RESET signal - Many of the HCS08 or Coldfire V1 microcontrollers do not have a dedicated reset signal. This is not a problem as it is possible to reset the target using the BKGD mode commands through the BDM interface. However, the RESET signal on the BDM interface will often still be connected to the Reset signal of the processor. This option allows the BDM to monitor and control the RESET signal when appropriate.

13) The WTBDMS08 will now connect to the target board.

The WTBDMS08 can now be used to debug and program the target micro.