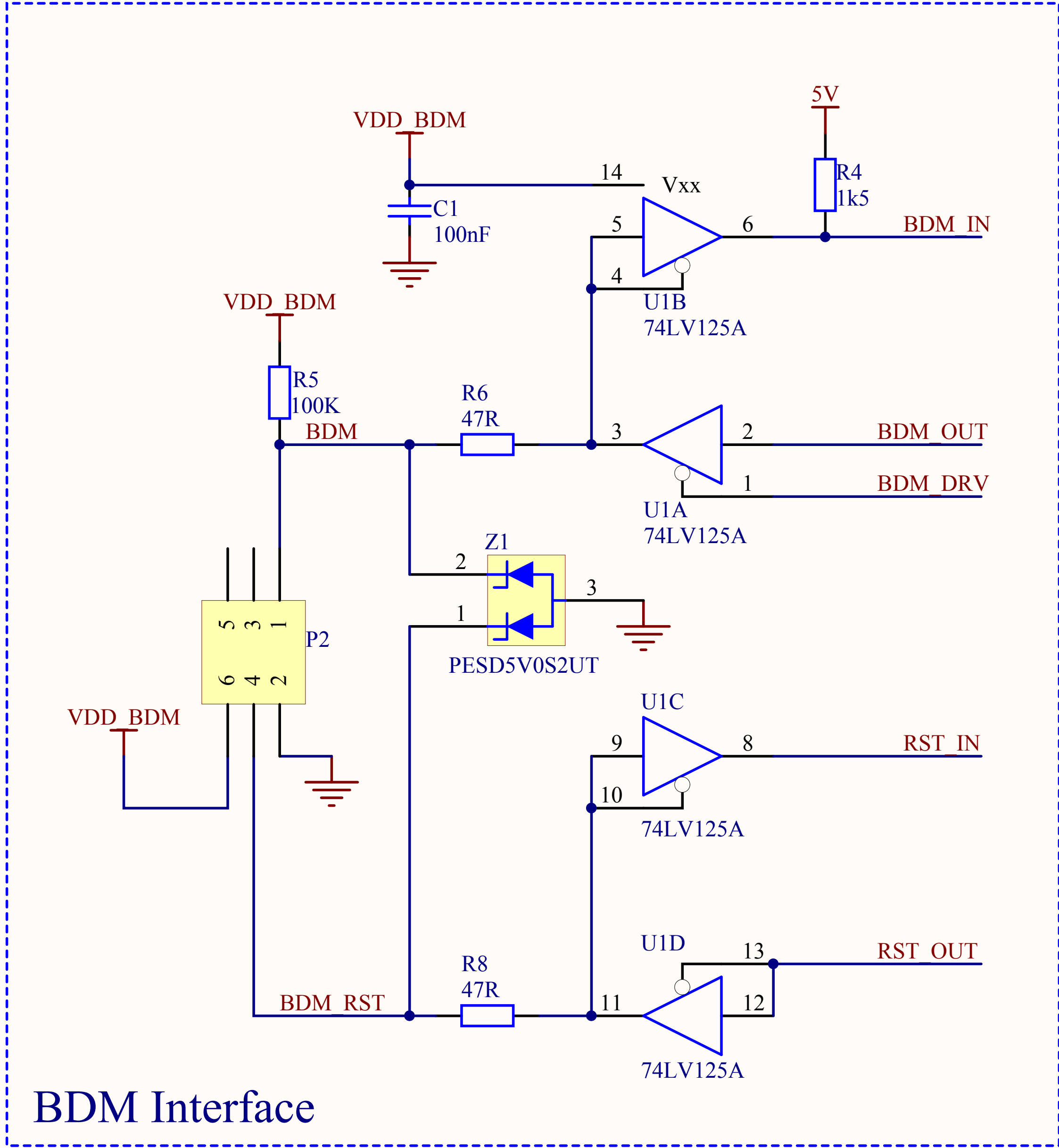
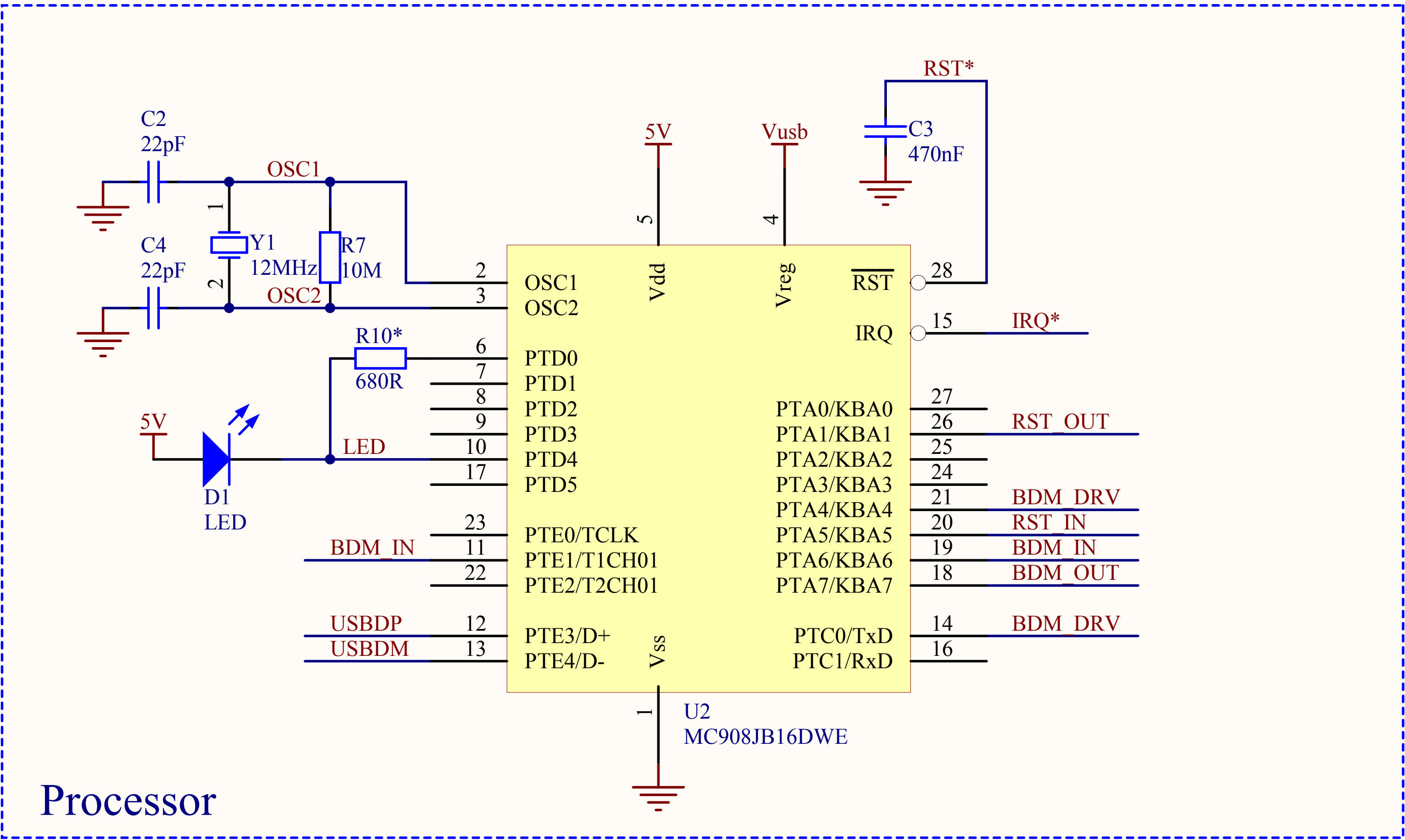


USB Interface

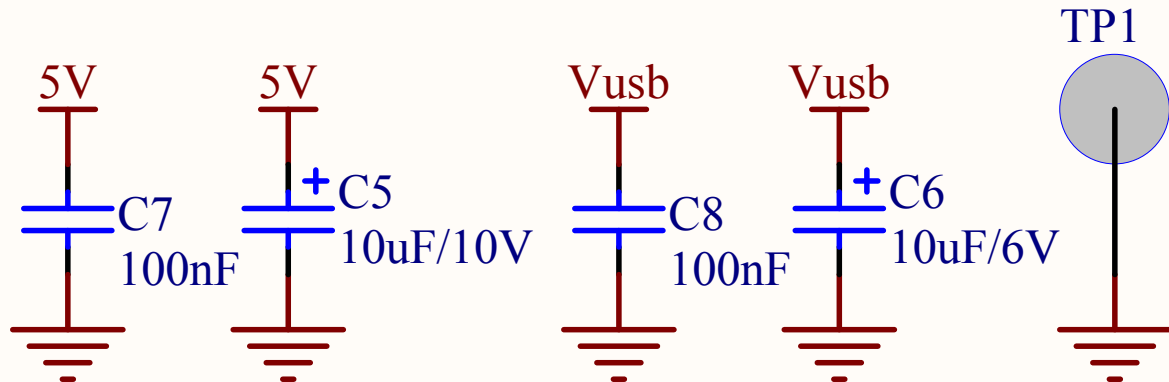


BDM Interface



Processor

Notes:  
\* This circuit may be adapted to the 20 pin version of the JB16 (JB16JDWE) or the 32 pin version (JB16FAE) by adding R10 and with obvious pin number changes (all other used pins apart from PTD4 are available on the 20p package).  
\* Z1 is a protection device and may be omitted.



Title TBDML_SOIC28_JB16(Minimal)		
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